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I THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Docket No.: 6192.0301.US

Yu-Ri SONG, et al.

Serial No.: 10/615,798

Group Art Unit: 2891

Filed: July 10, 2003

Examiner: FARAHANI, Dana

For: THIN FILM TRANSISTOR ARRAY

PANEL INCLUDING STORAGE ELECTRODE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL REPLY UNDER 37 C.F.R. 1.111(a)(2)

Sir:

Further to the July 11, 2005 telephone interview with the Examiner, please enter this supplemental reply in addition to Applicant's response filed July 12, 2005. Upon entry of this supplemental reply, please amend the above-identified application as set forth below.

Amendments to and a Listing of the Claims begin at page 2.

Remarks begin at page 6.

Conclusions begin at page 10.

The Applicant believes that no extensions of time are required at this time, but if extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. §1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to **Deposit Account No. 23-1951** (*McGuireWoods*). Please charge any deficiencies in fees and credit any overpayment of fees to the same Deposit Account.